

ABSTRACT OF THE DISCLOSURE

An input circuit according to the present invention has a data input means for the input of input data; a data latch means for latching the input data; a reset means for resetting
5 the data latch means; a clock synchronization means for synchronizing the input of the input data to the data input means; and a latch enhancement means for blocking feedthrough current by functioning complementarily to the reset means, and enhancing the latching operation of the data latch means.